



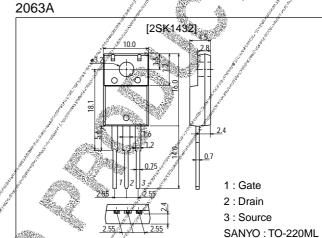
Ultrahigh-Speed Switching Applications

Features

- · Low ON-state resistance.
- · Ultrahigh-speed switching.
- · Converters.
- \cdot Micaless package facilitating easy mounting.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	Vosš /	100	V
Gate-to-Source Voltage	VGSS	±20	V
Drain Current (DC)	/,6	25	Α
Drain Current (Pulse)	/ I _{DP} PW≥10us, duty cycle≤1%	100	Α
Allowable Power Dissipation	Tc=25°C	40	W
		2.0	W
Channel Temperature	// Ich	150	°C
Storage Temperature	// State //	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	Jo=1mA, V _{GS} =0	100			V
Zero-Gate Voltage Drain/Current	l _{DSS} //	V _{DS} =100V, V _{GS} =0			100	μΑ
Gate-to-Source Leakage Current	IG\$S.	V _{GS} =±20V, V _{DS} =0			±100	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.5		2.5	V
Forward Transfer Admittance	/ lyfs	V _{DS} =10V, I _D =20A	13	22		S
Static Drain-to-Source ON-State Resistance	RDS(on)	I _D =20A, V _{GS} =10V		0.040	0.055	Ω

(Note) Be careful in handling the 25k 1432 because it has no protection diode between gate and source.

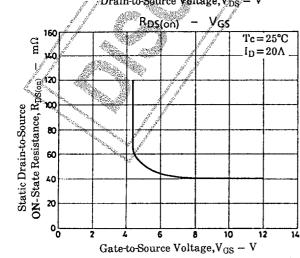
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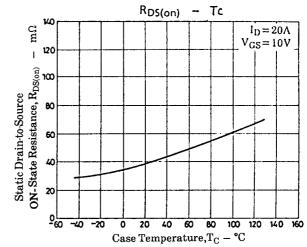
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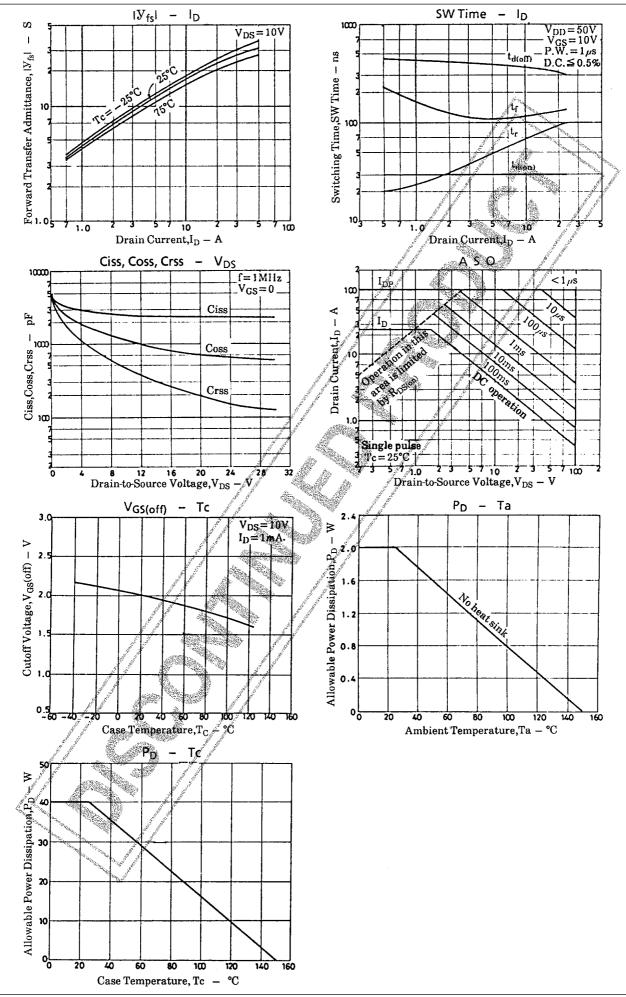
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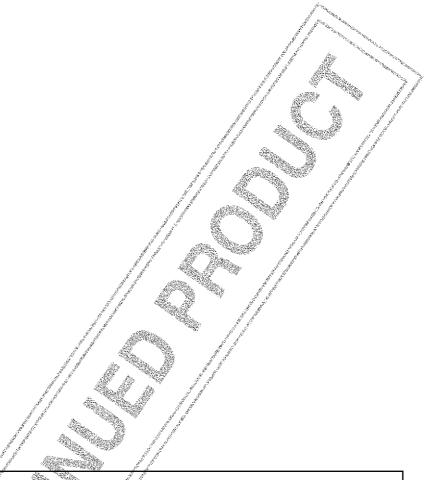
Parameter	Symbol	Conditions			Ratings		
Farameter		Conditions		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz			2400		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz			700		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		100	200		pF
Turn-ON Delay Time	t _d (on)	I_D =20A, V_{GS} =10V, V_{DD} =50V, R_{GS} =50 Ω		A Strain Land	30		ns
Rise Time	t _r	I_D =20A, V_{GS} =10V, V_{DD} =50V, R_{GS} =50 Ω	J.	1 /	.90	(SA)	ns
Turn-OFF Delay Time	td(off)	I_{D} =20A, V_{GS} =10V, V_{DD} =50V, R_{GS} =50 Ω	ight of the	× 4	320	Cappy Many and State Confession of the Confessio	ns
Fall Time	t _f	I_D =20A, V_{GS} =10V, V_{DD} =50V, R_{GS} =50 Ω	They stay	42	130	A. S. C. S.	ns
Diode Forward Voltage	V _{SD}	I _S =25A, V _{GS} =0	11	4 2000		1.8	V

Switching Time Test Circuit ۷_{DD} 50۷ P.W. = 1 #s D.C. ≦0.5% RL | ID 2.50 20A V_{GS} Yout 2SK1432 A_{GS} 500 - VGS V_{DS} I_D ID $V_{DS} = 10V$ 6.5 V 6V Drain Current, $I_D - A$ 5.5V Drain Current, ID 4.5V 10 3.5V ž 4 6 8 Drain-to-Source Voltage, V_{DS} – V Gate-to-Source Voltage, $V_{\rm GS}-V$ Ros(on) -R_{DS(on)} - Tc









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