TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSII)

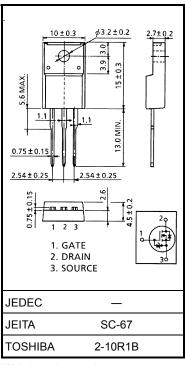
2SK3662

Switching Regulator, DC–DC Converter, Motor Drive Applications

- Low drain-source ON resistance: RDS (ON) = 9.4 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 55 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 60 \ V)$
- Enhancement mode : V_{th} = 1.3 to 2.5 V (V_{DS} = 10 V, I_D = 1 mA)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	60	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	60	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	I _D	35	А
	Pulse (Note 1)	I _{DP}	105	A
Drain power dissipation (Tc = 25° C)		PD	35	W
Single pulse avalanche energy (Note 2)		E _{AS}	204	mJ
Avalanche current		I _{AR}	35	А
Repetitive avalanche energy (Note 3)		E _{AR}	3.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	–55 to 150	°C



Weight: 1.9 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	3.57	°C/W
Thermal resistance, channel to ambient	R _{th (ch−a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25^{\circ}C (initial), L = 227 $\mu\text{H},~\text{I}_{AR}$ = 35 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Unit: mm

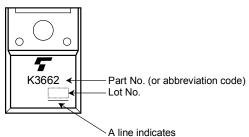
Electrical Characteristics (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_	_	±10	μA
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	100	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_		V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	40	_		
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	1.3	—	2.5	V
Drain-source ON resistance		Decement	V _{GS} = 4 V, ID = 18 A		12.5	19	mΩ
		R _{DS} (ON)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 18 \text{ A}$	_	9.4	12.5	
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 18 \text{ A}$	28	55		S
Input capacitance Reverse transfer capacitance Output capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	5120		pF
		C _{rss}			300		
		C _{oss}		_	500		
Switching time	Rise time	tr	$V_{GS} = 18 \text{ A} \text{Vout}$ $V_{GS} = V_{OUT}$ $V_{GS} = V_{OUT}$ $V_{DD} \approx 30 \text{ V}$ $V_{DU} \approx 1\%, t_{W} = 10 \mu\text{s}$	_	6		
	Turn-on time	t _{on}		_	19	_	ns
	Fall time	t _f		_	20		
	Turn-off time	t _{off}		_	115		
Total gate charge (gate-source plus gate-drain)		Qg	V ~ 49.V/ V 10.V/	_	91	_	nC
Gate-source charge		Q _{gs}	V _{DD} ≈ 48 V, V _{GS} = 10 V, I _D = 35 A	_	70	_	
Gate-drain ("miller") charge		Q _{gd}		_	21	_	

Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

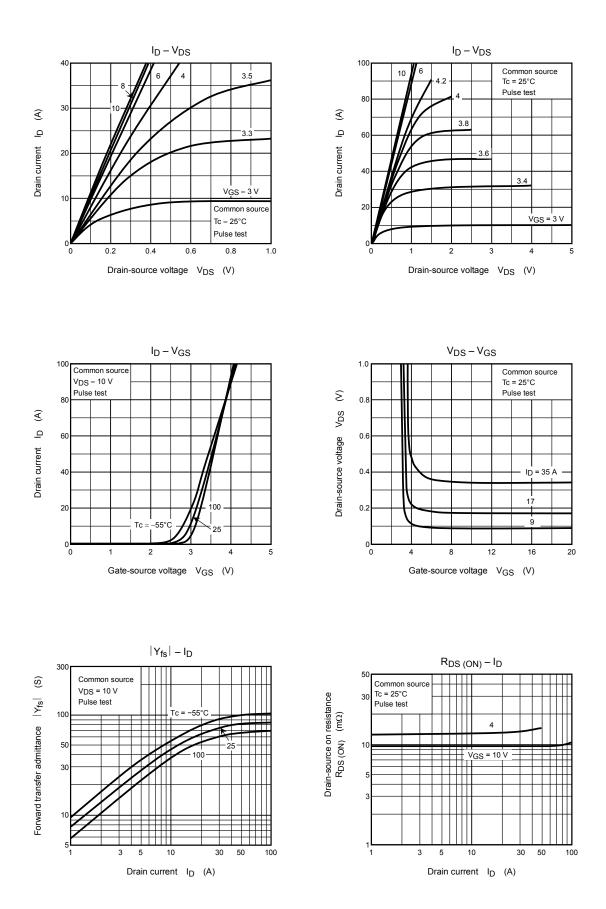
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—		_	35	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	105	А
Forward voltage (diode)	V _{DS2F}	$I_{DR1} = 35 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.5	V
Reverse recovery time	t _{rr}	$I_{DR} = 35 \text{ A}, V_{GS} = 0 \text{ V},$		60	_	ns
Reverse recovery charge	Q _{rr}	$dI_{DR}/dt = 50 \text{ A}/\mu \text{s}$		58		nC

Marking

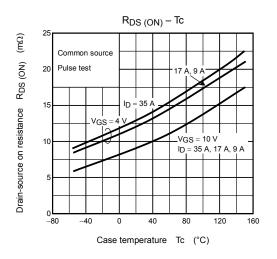


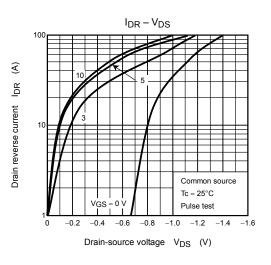
A line indicates lead (Pb)-free package or lead (Pb)-free finish.

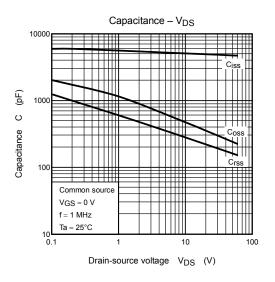
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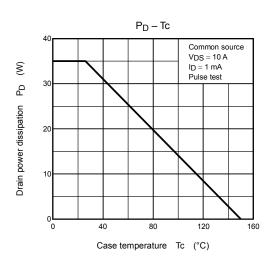


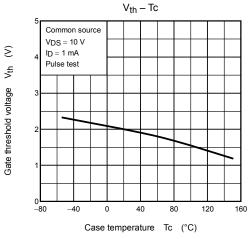
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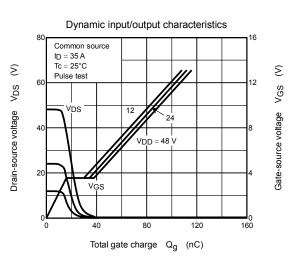


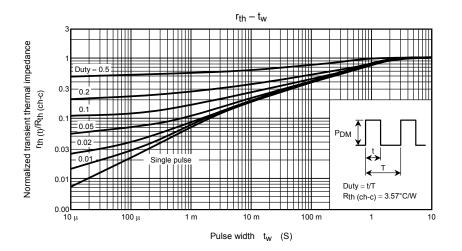


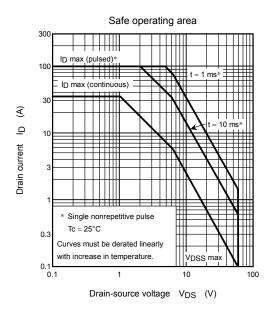


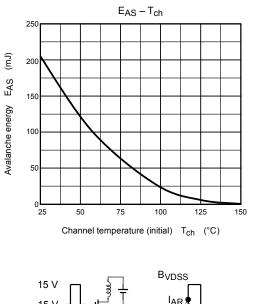


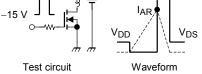












$R_G = 25 \Omega$	$EAC = \frac{1}{2} \cdot I \cdot I^2$	BVDSS BVDSS-VDD		
$V_{DD}=25~V,~L=227~\mu H$	LAS ²	(BVDSS-VDD)		

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