STK392-010



# 3-Channel Convergence Correction Circuit (Ic max = 5A)

# Overview

The STK391-010 is a convergence correction circuit IC for video projectors. It incorporates three output amplifiers in a single package, making possible the construction of CRT horizontal and vertical convergence correction output circuits for each of the RGB colors using ust two hybrid ICs.

### Applications

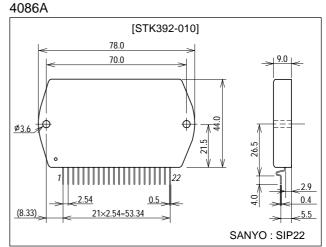
• General video projectors

## **Features**

- 3 output amplifier circuits in a single package (22-pin)
- High absolute maximum supply voltage ( $V_{CC}$  max =  $\pm 38V$ )
- Low thermal resistance ( $\theta j$ -c=2.6°C/W)
- High temperature stability (T<sub>C</sub> max=125°C)
- Separate predriver and output stage supplies
- Output stage supply switching for high-performance designs
- Pins are arranged in separate groups of inputs, supply, and outputs to reduce the adverse effects of pattern layout on characteristics and to make design easier.
- Constant-current circuit in the predriver for stable supply switching operation
- Large lineup of family devices (STK392-000 series) to cover the range from general applications to high-class applications using a single PCB

# **Package Dimensions**

# unit:mm



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# Specifications

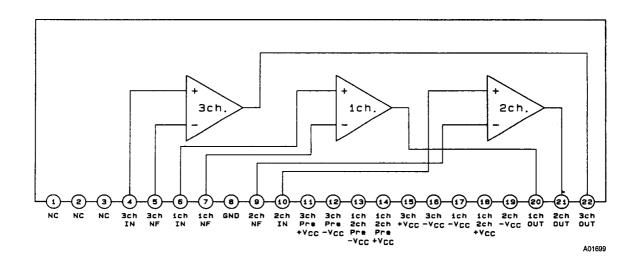
# **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		±38	V
Maximum collector current	ΙC	Tr8, 10, 18, 20, 28, 30	5.0	A
Thermal resistance	Ө ј-с	Tr8, 10, 18, 20, 28, 30 (per transistor)	2.6	°C/W
Junction temperature	Tj		150	°C
Operating temperature	Tc		125	°C
Storage temperature	Tstg		-30 to +125	°C

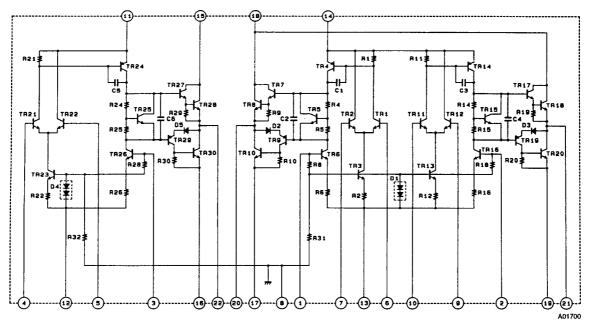
### **Operating Characteristics** at $Ta = 25^{\circ}C$ , $Rg=50\Omega$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Output noise voltage	VNO	V <sub>CC</sub> =±30V			0.2	mVrms
Quiescent current	Icco	V <sub>CC</sub> =±30V	30	90	150	mA
Neutral voltage	VN	V <sub>CC</sub> =±30V	-50	0	+50	mV
Output delay time	<sup>t</sup> D	V <sub>CC</sub> =±30V, f=15.75kHz, triangular wave input, V <sub>OUT</sub> =1.5Vp-p			1.0	μs
Frequency response	fH	$V_{CC}$ =±30V, -3dB, (0dB at 1kHz), sine wave input, $V_{in}$ =50mVp-p		1.8		MHz

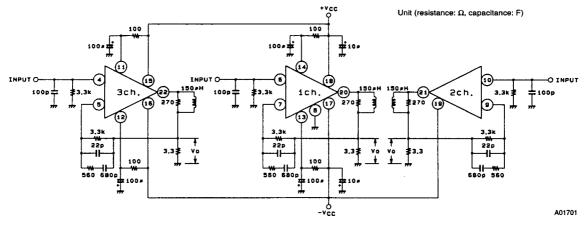
### **Block Diagram**

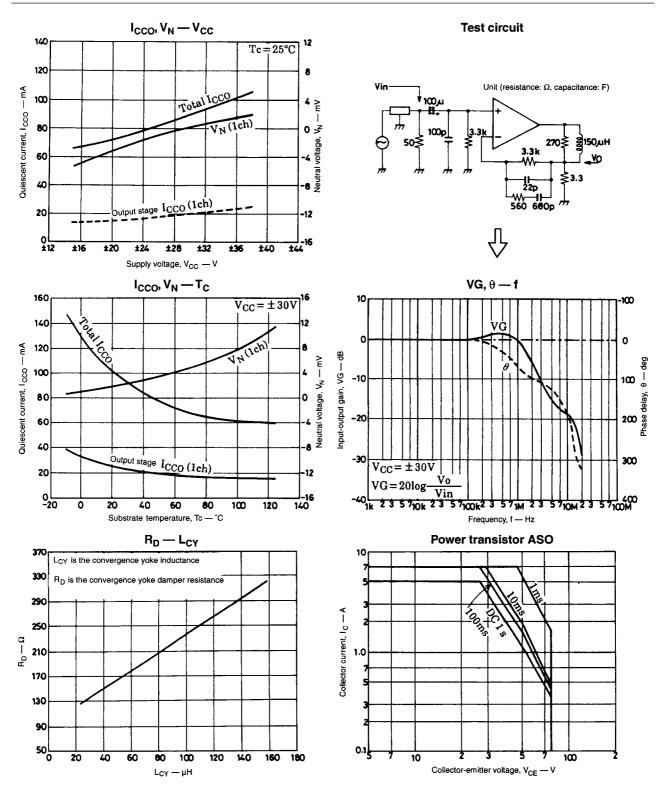


#### **Equivalent Circuit**



### **Test Circuit**





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