STK405-100



# 2ch AF Power Amplifier (Split Power Supply) (60W + 60W min, THD = 10%)

### Preliminary

### Overview

The STK405-100, a member of the STK405-000 series, is a low-cost, 2-channel audio power amplifier hybrid IC that is ideal for a wide range of stereo sets. It has dedicated  $6\Omega$ output drive, in contrast with the STK401-000 series which supports  $6\Omega/3\Omega$  output drive.

### Features

- Class B amplifiers
- Output load impedance  $R_L=6\Omega$  support
- EIAJ-output compatible (f=1kHz, THD=10%)
- Low supply switching shock noise
- Pin assignment grouped into individual blocks of inputs, outputs and supply lines to minimize the adverse effects of pattern layout on operating characteristics
- External boostrap circuit not necessary
- Standby operation possible using external circuit
- Voltage gain VG=26dB for easy gain distribution within the set
- Member of 10W/ch to 80W/ch pin-compatible series.

### **Series Organization**

The following devices form a series with differing output capacity. Some of the following devices are under development. Contact your Sanyo sales representative if you require more detailed information.

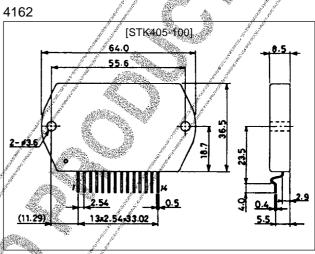
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Туре No.	Output power	Supply voltage [V]			
	<i>[ ]</i> [ ]	V <sub>CC</sub> max	VCC		
STK405-010	10W + 10W	±26.0	±14.0		
STK405-030	20W + 20W	<b>±30</b> .5 ⊘	<b>±18</b> .5		
STK405-050	30₩ <b>*</b> 30W	±34.5	<b>∳</b> 22.0		
STK405-070	40₩ + 40W	<b>±39</b> .0	£ ±25.0		
STK405-090	≠ 50W + 50₩	€_±42.0	∮ ±26.5		
STK405-100	/ 60W + 60W	±45.0	±29.0		
STK405-110	70W + 70W	±50,0	±31.0		
STK405-120	80M + 80M	±52.5	±33.0		
		and the second			

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## Package Dimensions





## Specifications

**Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions		Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max			±4	5.0 V
Thermal resistance	θ ј-с	Per power transistor	367	No. 1	.1 °C/W
Junction temperature	Tj		a Carl and	1	50 °C
Operating temperature	Тс		al and	· · · · · · · · · · · · · · · · · · ·	25 °C
Storage temperature	Tstg		State State	-30 to +1	25 °C
Available time for load short-circuit	t <sub>S</sub>	$V_{CC}=\pm 29.0V, R_{L}=6\Omega, f=50Hz, P_{O}=60W$	J.J.		1, ∫s

# **Operating Characteristics** at Ta = 25°C, $R_L=6\Omega$ (noninductive load), $R_g=600\Omega$ , VG=26dB

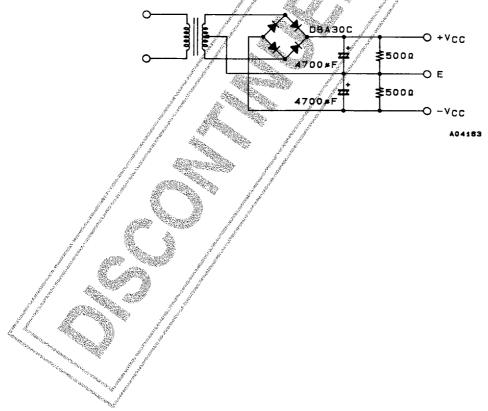
Parameter	Symbol	Conditions	min	Ratings typ	jimax	Unit
Quiescent current	Icco	V <sub>CC</sub> =±37.0V, no load	See.	13	20	mA
Output power	PO	V <sub>CC</sub> =±29.0V, f=1kHz, THD=10/0%	60 st		and the second se	W
Total harmonic distortion	THD	V <sub>CC</sub> =±29.0V, f=1kHz, P <sub>O</sub> =5:0W	00	0.04	0.1	%
Frequency response	f∟, f <sub>H</sub>	V <sub>CC</sub> =±29.0V, P <sub>O</sub> =1.0W, <sup>+0</sup> / <sub>-3</sub> dB		20 tố 50k		Hz
Input impedance	rj	V <sub>CC</sub> =±26.5V, f=1kHz, P <sub>O</sub> =1.0W		55		kΩ
Output noise voltage	V <sub>NO</sub>	V <sub>CC</sub> =±29.0V, Rg=10κΩ		and the second	1.2	mVrms
Neutral voltage	VN	V <sub>CC</sub> =±37.0V	-100	0	+100	mV

Note.

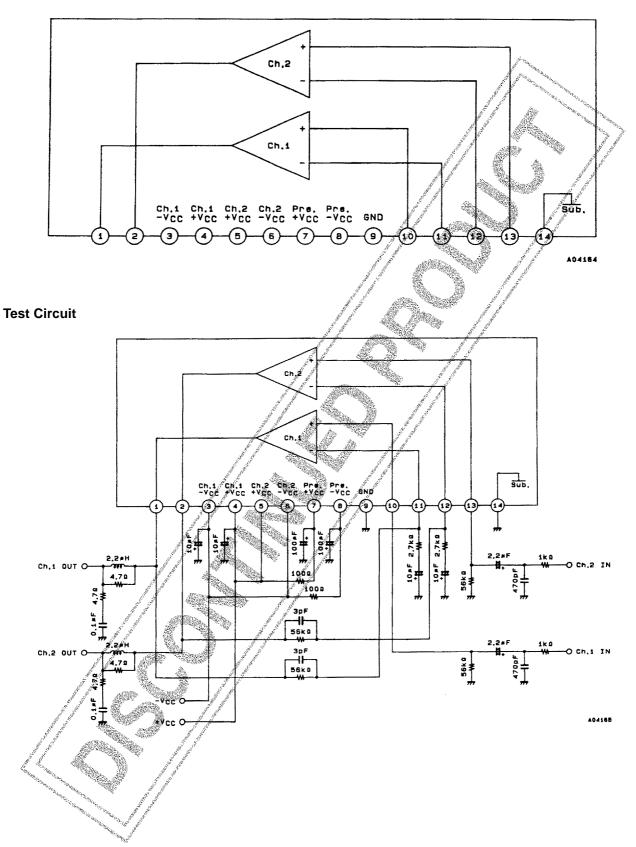
All tests are measured using a constant-voltage supply unless otherwise specified.

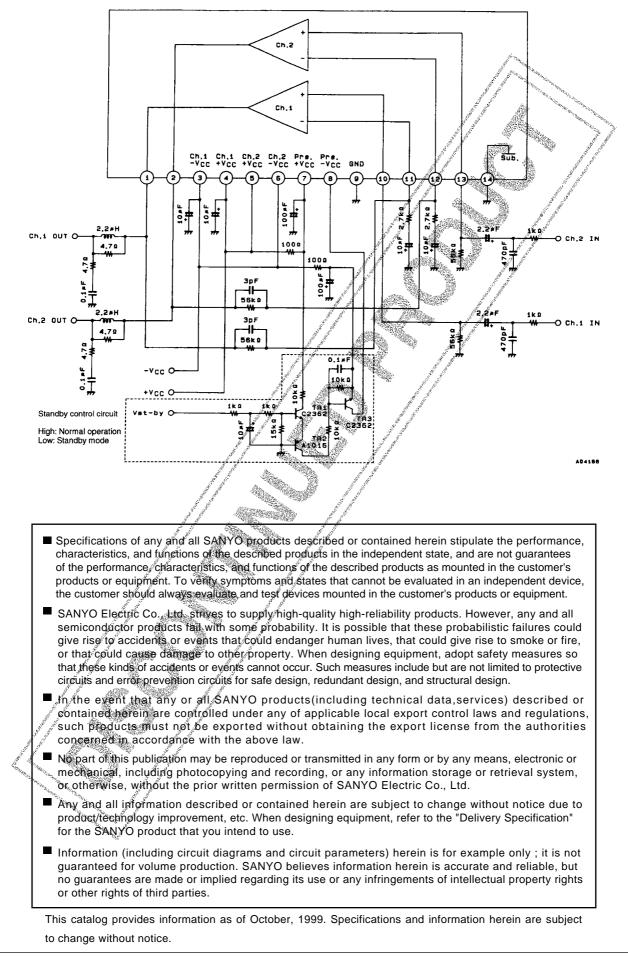
Available time for load short-circuit and output noise voltage are measured using the transformer supply specified below. The output noise voltage is the peak value of an average reading meter with an rms value scale (VTVM). A regulated AC supply (50Hz) should be used to eliminate the effects of AC primary line flicker noise.

### Specified Transformer Supply (RP-25 or Equivalent)



#### **Block Diagram**





#### Sample Application Circuit (Standby Mode Supported)